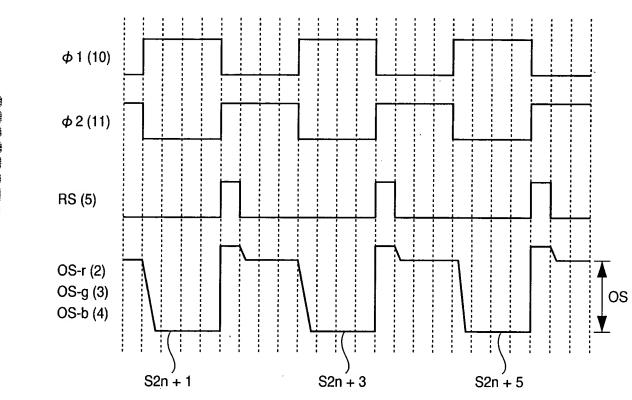
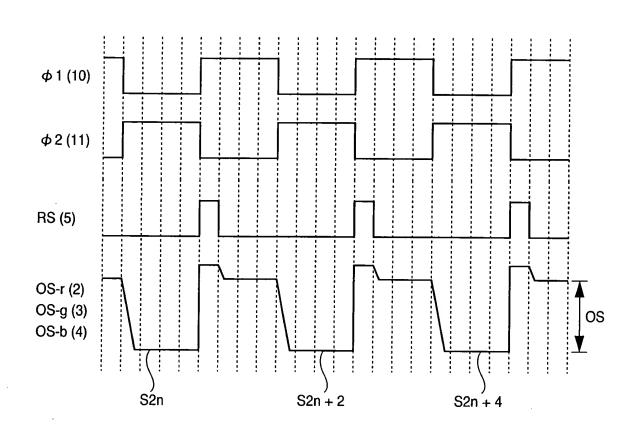
FIG. 1



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FIG. 2



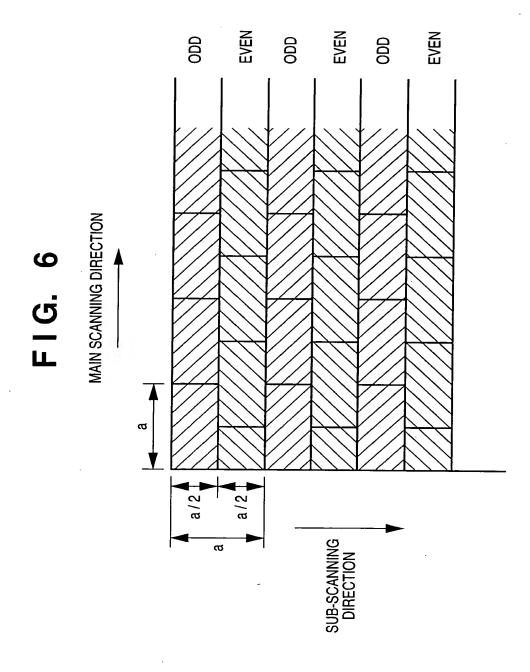
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T/2 EVEN T/2 000 φ5 ф1 몺 SO RS

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ф1 φ5 꼾 RS SO

1/2 000 T/2 ODD ф φ5 R SO RS



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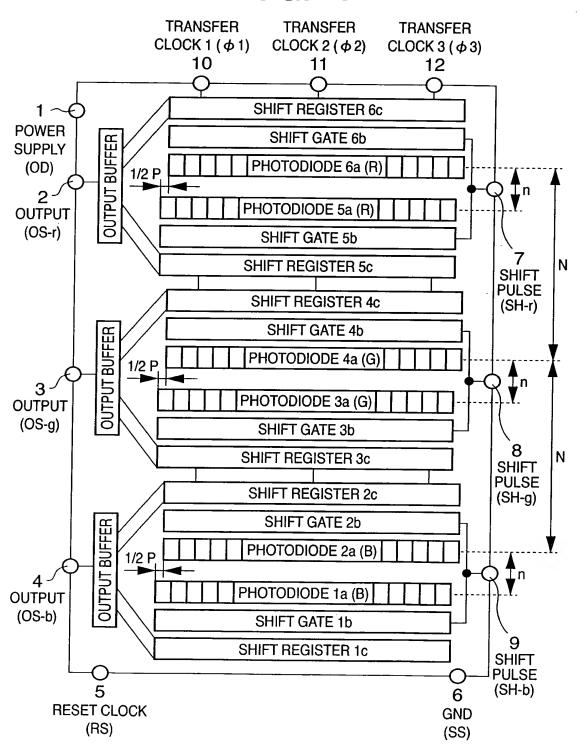
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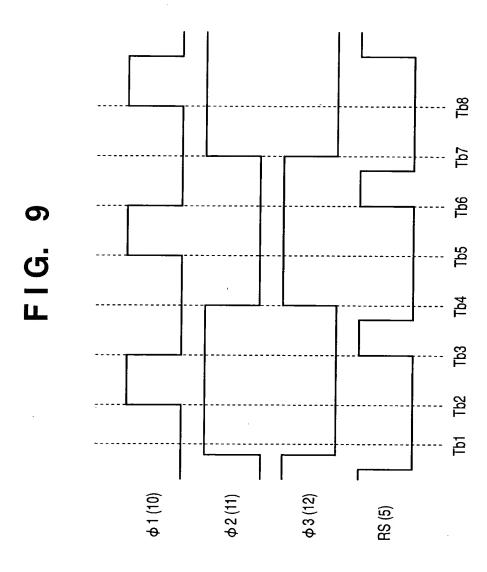
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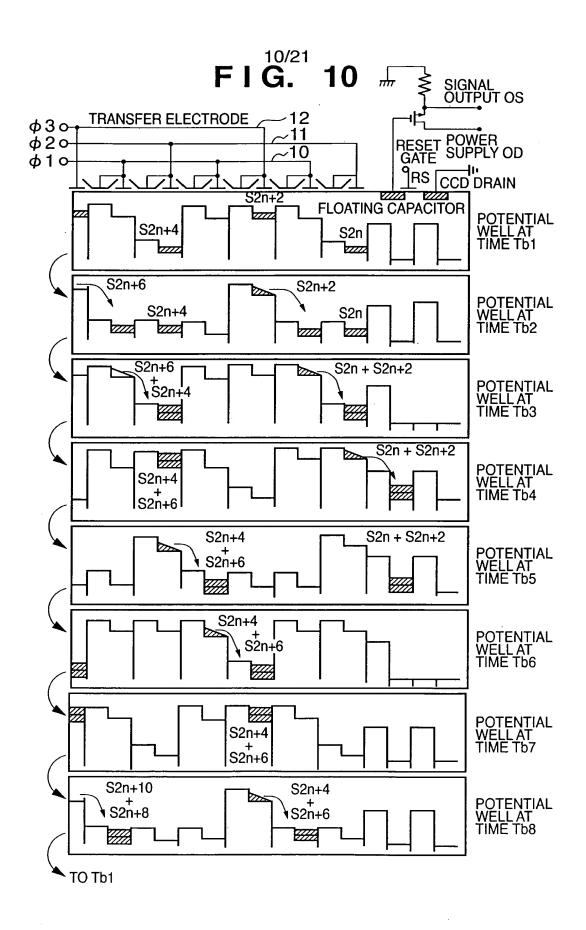
FIG. 7



→ SHIFT REGISTER → SHIFT REGISTER ► PHOTODIODE φ2φ3 s2n+2 s2n+4 s2n+1 s2n+3 : 88 **S**7 98 SS \$4 જુ \$2 S. 1/2 P







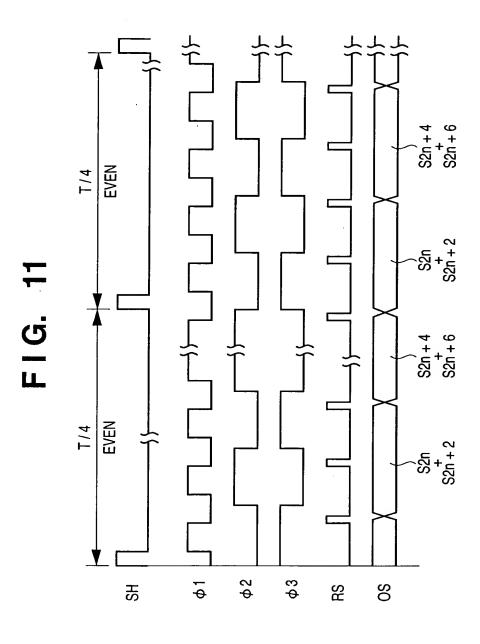
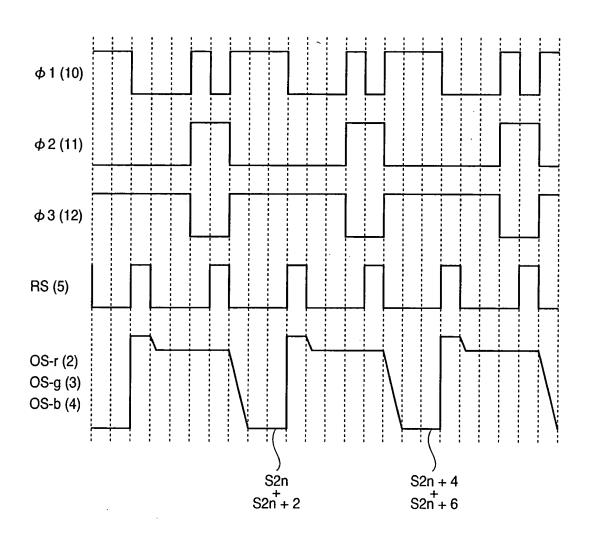


FIG. 12



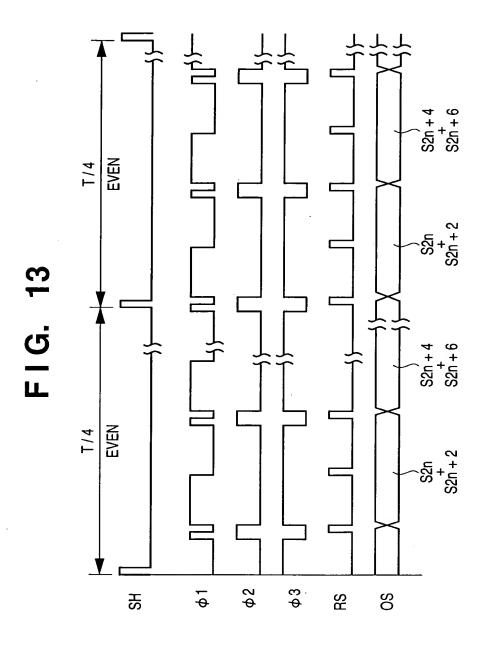


FIG. 14A

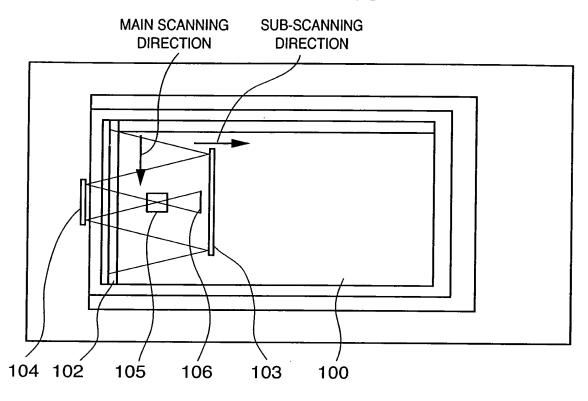
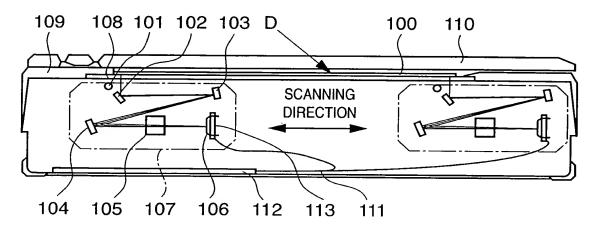
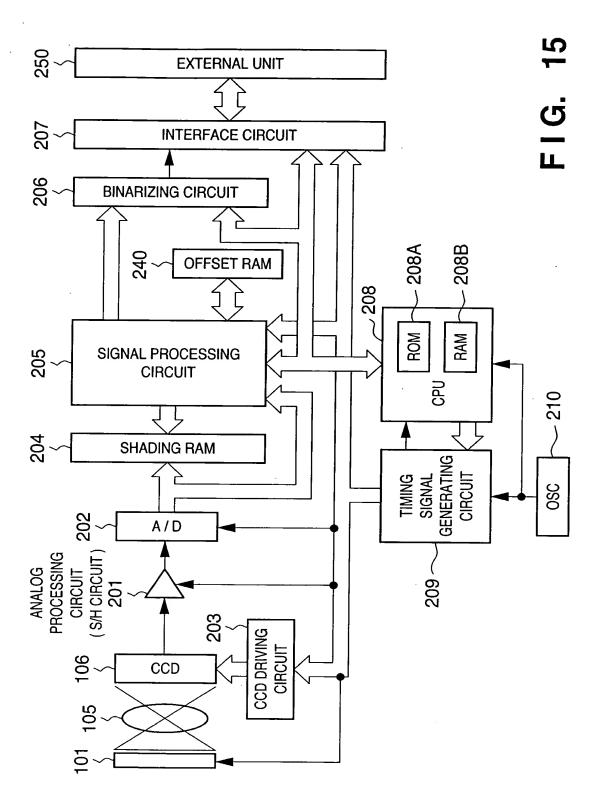


FIG. 14B



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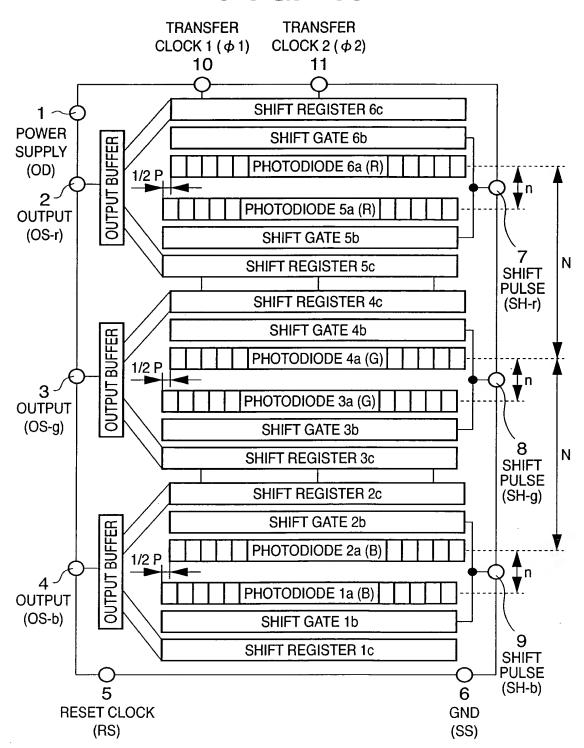
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1,7

<u>.</u>4

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FIG. 16



- SHIFT REGISTER → SHIFT REGISTER > PHOTODIODE φ2 φ1 s2n+2 s2n+4 s2n+1 s2n+3 : : ထ္တ **S**7 <u>s</u> SS **S**4 \aleph 82 s.

FIG. 18

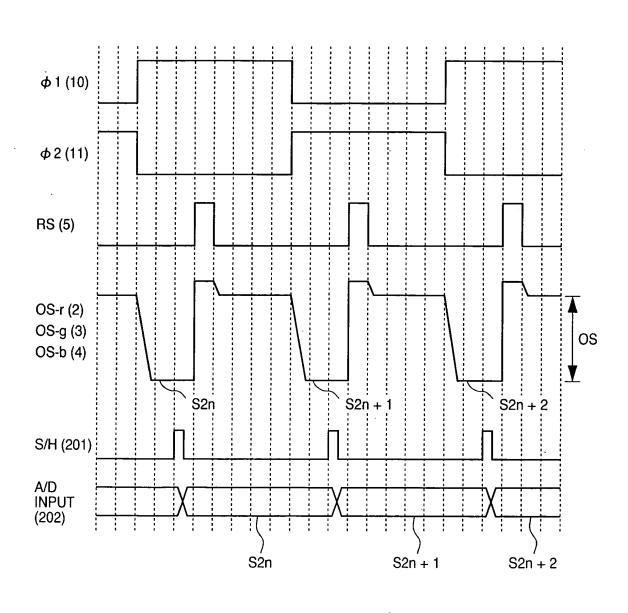
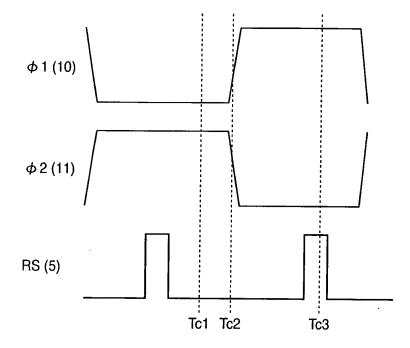


FIG. 19



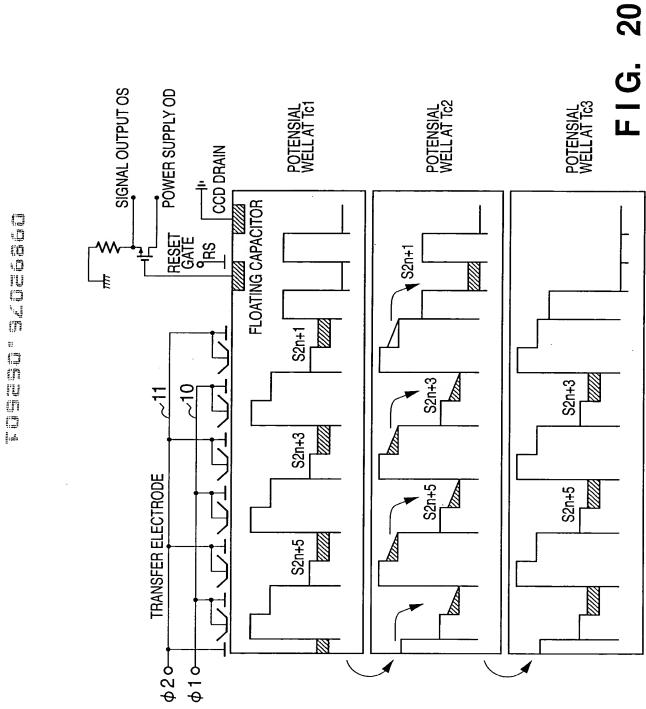


FIG. 21

